TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7PG34FU

#### **Dual NON-Inverter**

#### **Features**

High output current : ±8 mA (min) at V<sub>CC</sub> = 3 V

• Super high speed operation : t<sub>pd</sub> = 2.8 ns (typ.)

at  $V_{CC}$  = 3.3 V, 1  $C_L$  = 5 pF

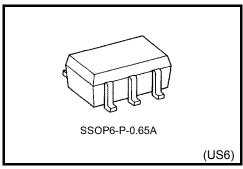
Operating voltage range : V<sub>CC</sub> = 0.9 to 3.6 V

• 5.5-V tolerant inputs

3.6-V power down protection outputs

• ESD performance : Machine model ≥ ±200 V

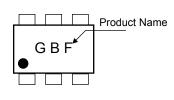
Human body model ≥ ±2000 V

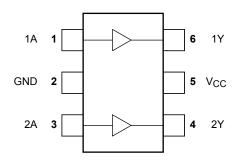


Weight: 6.8 mg (typ.)

#### Marking

## Pin Assignment (top view)





## **Absolute Maximum Ratings (Ta = 25°C)**

Characteristic	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	- 0.5 to 4.6	V
DC input voltage	V <sub>IN</sub>	– 0.5 to 7.0	V
DC output voltage	V <sub>OUT</sub>	– 0.5 to 4.6 (Note 1)	<b>V</b>
		-0.5 to V <sub>CC</sub> + 0.5 (Note 2)	V
Input diode current	I <sub>IK</sub>	-20	mA
Output diode current	lok	-20 (Note 3)	mA
DC output current	lout	± 25	mA
DC V <sub>CC</sub> /GND current	Icc	±100	mA
Power dissipation	PD	200	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

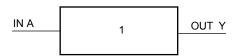
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{CC} = 0 V$ 

Note 2: High or Low State. Do not exceed  $I_{OUT}$  of absolute maximum ratings.

Note 3: V<sub>OUT</sub> < GND

# **IEC Logic Symbol**



### **Truth Table**

А	Υ
L	L
Н	Н

# **Operating Ranges**

Characteristic	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	. 0.9 to 3.6	V	
Input voltage	V <sub>IN</sub>	0 to 5.5	V	
Output voltage	Vour	0 to 3.6 (Note 4)	V	
	V <sub>OUT</sub>	0 to V <sub>CC</sub> (Note 5)		
Output current		± 8.0 (Note 6)	mA	
	I <sub>OH</sub> /I <sub>OL</sub>	± 4.0 (Note 7)		
		± 3.0 (Note 8)		
		± 1.7 (Note 9)	IIIA	
		± 0.3 (Note 10)		
		± 0.02 (Note 11)		
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 12)	ns/V	

Note 4:  $V_{CC} = 0 V$ 

Note 5: High or Low state.

Note 6:  $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ 

Note 7:  $V_{CC} = 2.3$  to 2.7 V

Note 8:  $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$ 

Note 9:  $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$ 

Note 10:  $V_{CC} = 1.1 \text{ to } 1.3 \text{ V}$ 

Note 11:  $V_{CC} = 0.9 \text{ V}$ 

Note 12:  $V_{IN} = 0.8$  to 2.0 V,  $V_{CC} = 3.0$  V

## **Electrical Characteristics**

### **DC Characteristics**

Characteristic Symbol Test Condition			Ta = 25°C		Ta = -40 to 85°C		Unit			
		l est Condition		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
				0.9	$V_{CC}$	_	_	V <sub>CC</sub>	_	
High-level V <sub>IH</sub> input voltage	1.1 to 1.3			V <sub>CC</sub> × 0.7	ı	ı	V <sub>CC</sub> × 0.7	l		
	V <sub>IH</sub>	_		1.4 to 1.6	V <sub>CC</sub> × 0.65		l	V <sub>CC</sub> × 0.65		V
				1.65 to 1.95	V <sub>CC</sub> × 0.65		l	V <sub>CC</sub> × 0.65		
				2.3 to 2.7	1.7	_	_	1.7		
					2.0	_	_	2.0	_	
		_		0.9		_	GND	_	GND	
				1.1 to 1.3			V <sub>CC</sub> × 0.3	_	$\begin{array}{c} V_{CC} \\ \times \ 0.3 \end{array}$	
Low-level	VIL			1.4 to 1.6			V <sub>CC</sub> × 0.35	_	V <sub>CC</sub> × 0.35	
input voltage			1.65 to 1.95	l	ı	V <sub>CC</sub> × 0.35	_	V <sub>CC</sub> × 0.35		
				2.3 to 2.7	1	1	0.7		0.7	
				3.0 to 3.6			0.8		0.8	
			I <sub>OH</sub> =-0.02 mA	0.9	0.75	_	_	0.75		
		V <sub>IN</sub> = V <sub>IH</sub>	$I_{OH} = -0.3 \text{ mA}$	1.1 to 1.3	V <sub>CC</sub> × 0.75			V <sub>CC</sub> × 0.75		V
High-level	V <sub>OH</sub>		I <sub>OH</sub> = -1.7 mA	1.4 to 1.6	V <sub>CC</sub> × 0.75	_	_	V <sub>CC</sub> × 0.75	_	
output voltage			I <sub>OH</sub> = -3.0 mA	1.65 to 1.95	V <sub>CC</sub> -0.45	_	_	V <sub>CC</sub> -0.45	_	
			I <sub>OH</sub> = -4.0 mA	2.3 to 2.7	2.0	_	_	2.0	_	
			I <sub>OH</sub> = -8.0 mA	3.0 to 3.6	2.48	_	_	2.48	_	
			$I_{OL} = 0.02 \text{ mA}$	0.9		_	0.1	_	0.1	V
			I <sub>OL</sub> = 0.3 mA	1.1 to 1.3	_	_	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	
Low-level V <sub>O</sub>	V <sub>OL</sub>	$V_{IN} = V_{IL}$	I <sub>OL</sub> = 1.7 mA	1.4 to 1.6		_	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	
			I <sub>OL</sub> = 3.0 mA	1.65 to 1.95	_	_	0.45	_	0.45	
			I <sub>OL</sub> = 4.0 mA	2.3 to 2.7	_	_	0.4	_	0.4	
			I <sub>OL</sub> = 8.0 mA	3.0 to 3.6	_	_	0.4	_	0.4	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 5.5V		0 to 3.6			±0.1	_	±1.0	μА
Power off leakage current	l <sub>OFF</sub>	V <sub>IN</sub> = 0 to 5.5V V <sub>OUT</sub> = 0 to 3.6V		0.0			1.0	_	10.0	μА
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		3.6	_	_	1.0	_	10.0	μА

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## AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristic	Symbol	Test Condition		Ta = 25°C		Ta = -40 to 85°C		Unit	
Symbol		rest Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
		$C_L$ = 10 pF, $R_L$ = 1 M $\Omega$	0.9	_	27.2	_	_	_	
			1.1 to 1.3	_	12.2	23.2	1.0	42.6	
			1.4 to 1.6	1	6.5	10.2	1.0	12.0	
			1.65 to 1.95	1	4.7	7.0	1.0	7.6	
			2.3 to 2.7	1	3.1	4.4	1.0	4.9	
			3.0 to 3.6	1	2.4	3.5	1.0	4.1	
			0.9	1	29.8		_	_	
	<sup>t</sup> pLH <sup>t</sup> pHL	$C_L$ = 15 pF, $R_L$ = 1 M $\Omega$	1.1 to 1.3		13.5	26.0	1.0	44.5	ns
Propagation delay time			1.4 to 1.6		7.2	11.4	1.0	13.6	
Tropagation delay time			1.65 to 1.95		5.2	7.5	1.0	7.7	
			2.3 to 2.7		3.4	4.8	1.0	5.5	
			3.0 to 3.6		2.8	3.8	1.0	4.4	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		40.7		_	_	
			1.1 to 1.3		17.8	33.9	1.0	64.1	
			1.4 to 1.6		9.1	14.3	1.0	17.4	
			1.65 to 1.95		6.6	9.8	1.0	10.2	
			2.3 to 2.7	_	4.1	6.2	1.0	6.6	
			3.0 to 3.6		3.3	4.8	1.0	5.2	
Input capacitance	C <sub>IN</sub>	_	3.6		3		_	_	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 13)	0.9 to 3.6	_	9	_	_	_	pF

Note 13: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

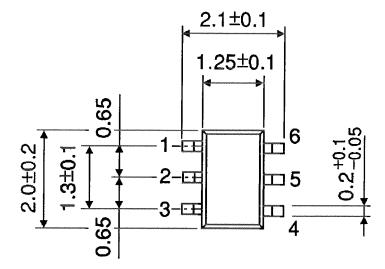
Average operating current can be obtained by the equation:

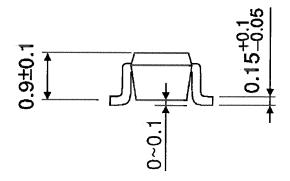
 $I_{CC \text{ (opr.)}} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$ 

# **Package Dimensions**

SSOP6-P-0.65A

Unit: mm





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Weight: 6.8 mg (typ.)

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